Amendments To The Abstract
CLEAN VERSION

Please replace the Abstract of the Disclosure with the following amended paragraph:

A DLL circuit synchronizes an external input clock applied from an outside of a system with an internal input clock used inside the system using a divider unit. The DLL circuit includes a detection unit for detecting whether a pulse width of the external input clock is narrower than a reference set value. The divider unit outputs a first divided signal when it is detected that the pulse width of the external input clock is wider than the reference set value, and outputs a second divided signal when it is detected that the pulse width of the external input clock is shorter than the reference set value. The DLL circuit can normally operate even when the period of the external input clock is short.

Amendments To The Abstract
MARKED-UP VERSION

The following marked-up version of the amended Abstract is attached hereto to aid the Examiner in identifying the changes:

A DLL circuit synchronizes an external input clock applied from an outside of a system with an internal input clock used inside the system using a divider unit. The DLL circuit includes a detection unit for detecting whether a pulse width of the external input clock is narrower than a reference set value. The divider unit outputs a first divided signal ~~if~~ **when** it is detected that the pulse width of the external input clock is wider than the reference set value, and outputs a second divided signal ~~if~~ **when** it is detected that the pulse width of the external input clock is shorter than the reference set value. The DLL circuit can normally operate even ~~if~~ **when** the period of the external input clock is short.